

# Based Material Line Up



## S1000/S1000B

### 1. CORE (C-STAGE)

Thickness		ply-up	RC (%)	Dk				Df			
mm	mil			1 GHz	3 GHz	5 GHz	10 GHz	1 GHz	3 GHz	5 GHz	10 GHz
0.050	2.00	1x1067	65	4.05	4.03	3.98	3.94	0.018	0.018	0.018	0.018
0.063	2.52	1x1078	60	4.19	4.19	4.14	4.11	0.016	0.018	0.017	0.017
0.076	3.04	1x1080	65	4.05	4.03	3.98	3.94	0.018	0.018	0.018	0.018
0.090	3.60	1X3313	52	4.45	4.43	4.38	4.36	0.015	0.016	0.016	0.016
0.100	4.00	1x2116	49	4.54	4.52	4.48	4.46	0.015	0.015	0.016	0.016
0.110	4.40	1x2116	50	4.51	4.49	4.44	4.42	0.015	0.015	0.016	0.016
0.125	5.00	1x2116	57	4.30	4.28	4.23	4.20	0.016	0.016	0.018	0.017
0.150	6.00	1x1506	46	4.63	4.61	4.57	4.55	0.014	0.015	0.015	0.015
0.200	8.00	1x7628	47	4.60	4.58	4.54	4.52	0.014	0.015	0.015	0.015
0.240	9.60	2x2116	55	4.36	4.34	4.29	4.26	0.016	0.016	0.016	0.017
0.300	12.00	2x1506	46	4.63	4.61	4.57	4.55	0.014	0.015	0.015	0.015
0.350	14.00	2x7628	43	4.72	4.70	4.66	4.65	0.014	0.014	0.014	0.014
0.400	16.00	2x7628	45	4.66	4.64	4.60	4.59	0.014	0.014	0.015	0.015
0.450	18.00	2X7628+2116	44	4.69	4.67	4.63	4.62	0.014	0.014	0.015	0.015
0.500	20.00	2X7628+2116	47	4.60	4.58	4.54	4.52	0.014	0.015	0.015	0.015
0.510	20.00	2X7628+2116	48	4.57	4.55	4.51	4.49	0.015	0.015	0.015	0.015
0.600	24.00	2X2116+2X7628	48	4.57	4.55	4.51	4.49	0.015	0.015	0.015	0.015
0.800	32.00	4X7628+1080	45	4.66	4.64	4.60	4.59	0.014	0.014	0.015	0.015
1.000	40.00	4X7628+2X1506	44	4.69	4.67	4.63	4.62	0.014	0.014	0.015	0.015

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1.090	43.00	6X7628	43	4.72	4.70	4.66	4.65	0.014	0.014	0.014	0.014
1.200	47.00	7X7628	42	4.75	4.73	4.69	4.68	0.014	0.014	0.014	0.014

## 2. PREPREG (B-STAGE)

Glass style	RC (%) Nominal	Thickness		Dk				Df			
		mm	mil	1GHz	3GHz	5GHz	10GHz	1 GHz	3 GHz	5 GHz	10 GHz
106	73	0.053	1.97	3.81	3.79	3.73	3.68	0.019	0.019	0.020	0.020
106	78	0.066	2.48	3.66	3.64	3.57	3.52	0.02	0.02	0.021	0.021
1080	63	0.073	2.76	4.13	4.13	4.08	4.04	0.017	0.018	0.017	0.017
1080	65	0.077	2.95	4.05	4.03	3.98	3.94	0.018	0.018	0.018	0.018
1080	68	0.086	3.23	3.96	3.94	3.89	3.84	0.018	0.018	0.019	0.019
2313	55	0.097	3.74	4.36	4.34	4.29	4.26	0.016	0.016	0.016	0.017
2313	58	0.106	3.94	4.26	4.25	4.20	4.17	0.016	0.017	0.017	0.017
2116	50*	0.109	4.09	4.51	4.49	4.44	4.42	0.015	0.015	0.016	0.016
2116	52*	0.115	4.33	4.45	4.43	4.38	4.36	0.015	0.016	0.016	0.016
2116	55	0.124	4.72	4.36	4.34	4.29	4.26	0.016	0.016	0.016	0.017
2116	58	0.135	5.12	4.26	4.25	4.20	4.17	0.016	0.017	0.017	0.017
1506	45	0.153	5.91	4.66	4.64	4.60	4.59	0.014	0.014	0.015	0.015
1506	48	0.165	6.30	4.57	4.55	4.51	4.49	0.015	0.015	0.015	0.015
7628	43	0.186	7.28	4.72	4.70	4.66	4.65	0.014	0.014	0.014	0.014
7628	45	0.195	7.60	4.66	4.64	4.60	4.59	0.014	0.014	0.015	0.015
7628	46	0.200	7.80	4.63	4.61	4.57	4.55	0.014	0.015	0.015	0.015
7628	47	0.205	7.95	4.60	4.58	4.54	4.52	0.014	0.015	0.015	0.015

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7628	50	0.220	8.58	4.51	4.49	4.44	4.42	0.015	0.015	0.016	0.016
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### 3. REMARK

- 1) Test by SPDR method.
- 2) The data above show actual values and are not guaranteed, for your reference only.
- 3) RC\* is not common type for reference.
- 4) Last update: September, 2020